

DASH Status Update SPPEXA Annual Plenary Meeting 2017 Garching, March 20-22, 2017



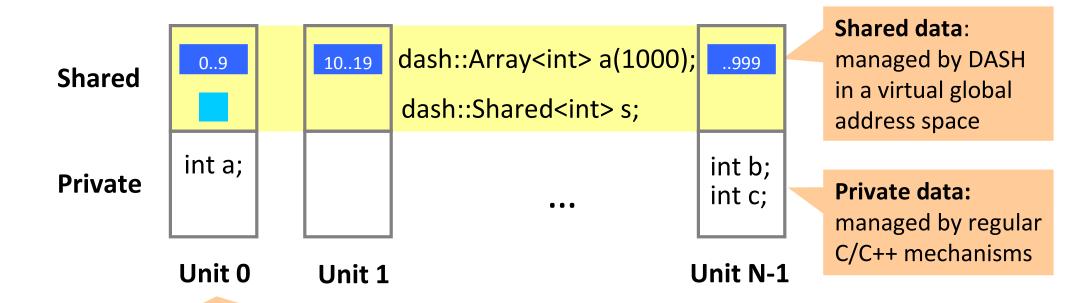
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DASH Overview

- DASH is a C++ template library that offers
 - 1. Distributed data structures and parallel algorithms that can be integrated in existing applications
 - 2. A complete PGAS (part. global address space) programming system without requiring a custom compiler



Units: The individual participants in a DASH program, usually full OS processes.



DASH Project Structure

Tools and Interfaces

DASH Appl	lication
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DASH C++ Template Library

DART API

DASH Runtime (DART)

One-sided Communication
Substrate

MPI GASnet ARMCI GASPI

Hardware: Network, Processor, Memory, Storage

	Phase I (2013-2015)	Phase II (2016-2018)
LMU Munich	Project management, C++ template library	Project management, C++ tempalte library, DASH data dock
TU Dresden	Libraries and interfaces, tools support	Smart data structures, resilience
HLRS Stuttgart	DASH runtime (DART)	DASH runtime (DART)
KIT Karlsruhe	Application case studies	
IHR Stuttgart		Smart deployment, Application case studies









Recent Activities

Features:

- DASH multi-dimensional array (dash::NArray) and data distribution patterns [1],[6]
- Application engagement: LULESH, others [2],[5],[7]
- Support for performance tools and debuggers with DASH [3]
- Correctness tool: Nasty-MPI [4]
- DART progress engine [8]
- Project management and outreach
 - Release of DASH v0.2.0 and move to GitHub
 - DASH Tutorial @HiPEAC in Stockholm
 - DASH presentations at LLNL, LBNL, ORNL, TU Wien, KTH Stockholm
 - DASH v0.3.0 release imminent



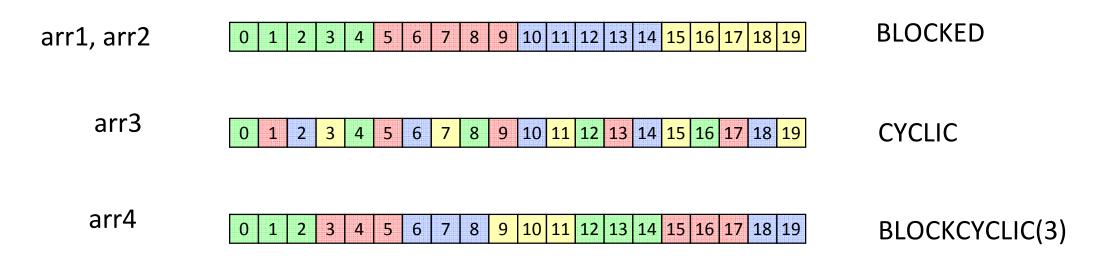
Data Distribution Patterns

The data distribution pattern is configurable

```
dash::Array<int> arr1(20); // default: BLOCKED

dash::Array<int> arr2(20, dash::BLOCKED)
dash::Array<int> arr3(20, dash::CYCLIC)
dash::Array<int> arr4(20, dash::BLOCKCYCLIC(3))
```

Assume 4 units

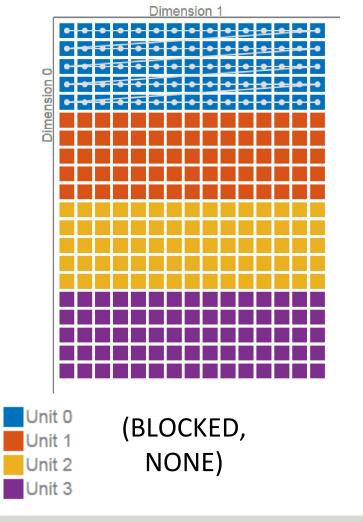




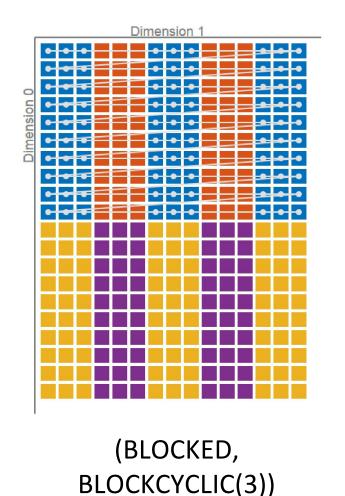
Multi-dimensional Data Distribution (1)

- dash::Pattern<N> specifies N-dim data distribution
 - Blocked, cyclic, and block-cyclic in multiple dimensions

Pattern<2>(20, 15)





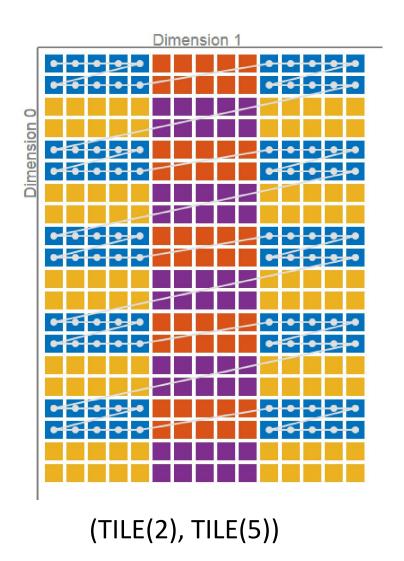




Multi-dimensional Data Distribution (2)

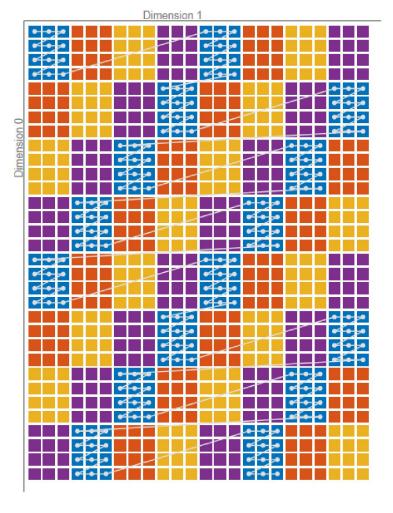
Tiled data distribution and tile-shifted distribution

TilePattern<2>(20, 15)





ShiftTilePattern<2>(32, 24)



(TILE(4), TILE(3))

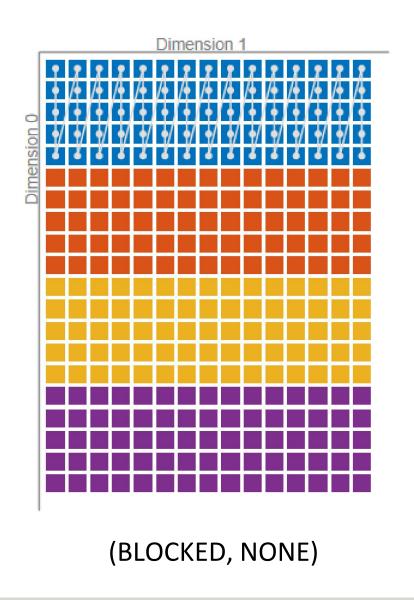


Multi-dimensional Data Distribution (3)

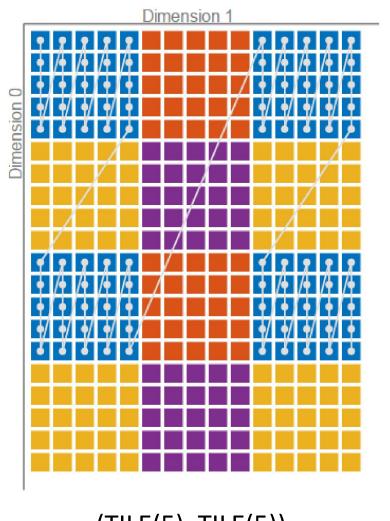
Row-major and column-major storage

Pattern<2, COL_MAJOR>(20, 15)

TilePattern<2, COL_MAJOR>(20, 15)







(TILE(5), TILE(5))



The Multi-Dimensional Array

- dash::NArray (dash::Matrix) offers a distributed multidimensional array abstraction
 - Dimension is a template parameter
 - Element access using coordinates or linear index
 - Support for custom index types
 - Support for row-major and column-major storage

```
dash::NArray<int, 2> mat(40, 30); // 1200 elements

int a = mat(i,j); // Fortran style access
int b = mat[i][j]; // chained subscripts

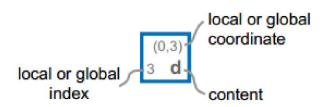
auto loc = mat.local;

int c = mat.local[i][j];
int d = *(mat.local.begin()); // local iterator
```

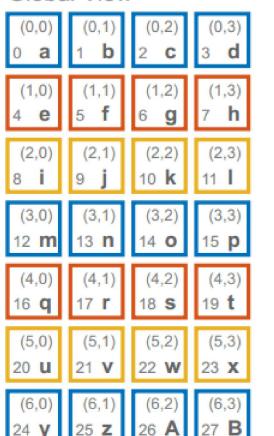


DASH NArray Global View and Local View

Local view works similar to 1D array

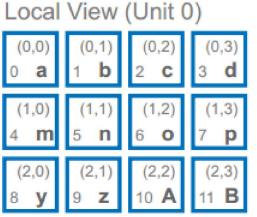


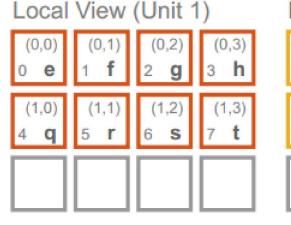
Global View

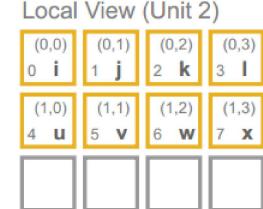


```
dash::NArray<char, 2> mat(7, 4);
cout << mat(2, 1) << endl; // prints 'j'

if(dash::myid()==0 ) {
  cout << mat.local(2, 1) << endl; // prints 'z'
}</pre>
```







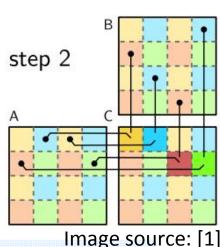


Application Example: S(R)UMMA Algorithm

Block matrix-matrix mulitplication algorithm with block prefetching
Decomposition
Prefetching

Prefetching

step 1

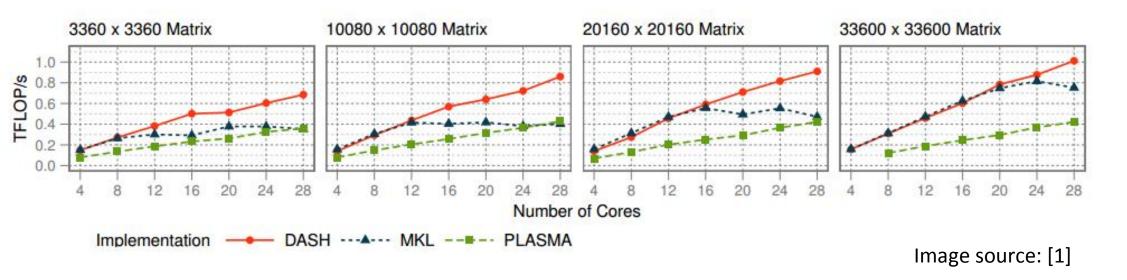


```
while(!done) {
   blk_a = ...
   blk_b = ...
   // prefetch
   auto get_a = dash::copy_async(blk_a.begin(), blk_a.end(), lblk_a_get);
   auto get_b = dash::copy_async(blk_b.begin(), blk_b.end(), lblk_b_get);
   // local DGEMM
   dash::multiply(lblk_a_comp, lblk_b_comp, lblk_c_comp);
   // wait for transfer to finish
   get_a.wait(); get_b.wait();
   // swap buffers
   swap(lblk_a_get, lblk_a_comp); swap(lblk_b_get, lblk_b_comp);
}
```



DGEMM on a Single Shared Memory Node

■ LRZ SuperMUC, phase 2: Haswell EP, 1.16 Tflop/sec peak

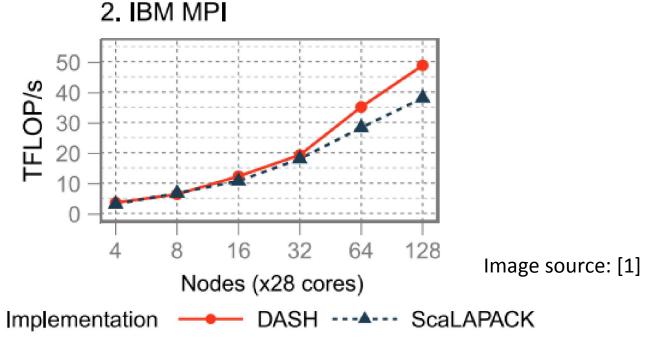


- DASH: Multi-process, using one-sided communication and singlethreaded MKL
- MKL: Multithreaded, using OpenMP
- PLASMA: Multithreaded tile-algorithms on top of sequential BLAS

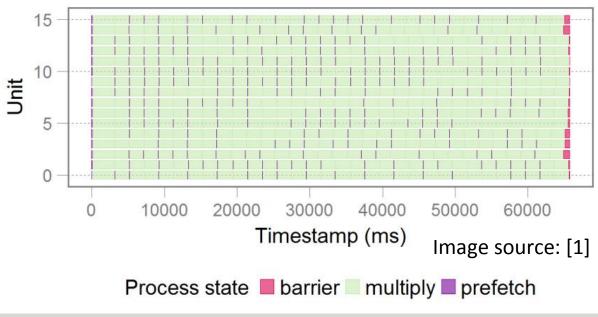


PDGEMM: DASH vs. ScaLAPACK Multinode

Strong scaling on SuperMUC (57344 × 57344 matrix)



Trace: Overlapping communication and computation





Porting LULESH to DASH (ongoing work)

- LULESH (Livermore Unstructured Lagrangian Explicit
 Shock Hydrodynamics code)
 - Widely used mini-app/proxy application
- Goals of the DASH port
 - Remove limitations of MPI domain decomposition approach
 - Avoid replication, manual index calculation, bookkeeping

```
if (rowMin | rowMax) {
  /* ASSUMING ONE DOMAIN PER RANK, CONSTANT BLOCK SIZE HERE */
   int sendCount = dx * dz :
                                                                 implicit assumptions
  if (rowMin) {
     destAddr = &domain.commDataSend[pmsq * maxPlaneComm];
     for (Index t fi=0; fi<xferFields; ++fi) {
        Domain member src = fieldData[fi];
        for (Index t i=0; i<dz; ++i) {
                                                                manual index calculation
           for (Index t j=0; j<dx; ++j) {
              destAddr[i*dx+j] = (domain.*src)(i*dx*dy + j) ;
        destAddr += sendCount ;
     destAddr -= xferFields*sendCount ;
                                                            manual msg bookkeeping
     MPI Isend(destAddr, xferFields*sendCount, baseType,
               myRank - domain.tp(), msqType,
                                                                               Replicated about 80 times
               MPI COMM WORLD, &domain.sendRequest[pmsq])
```



Porting LULESH (options)

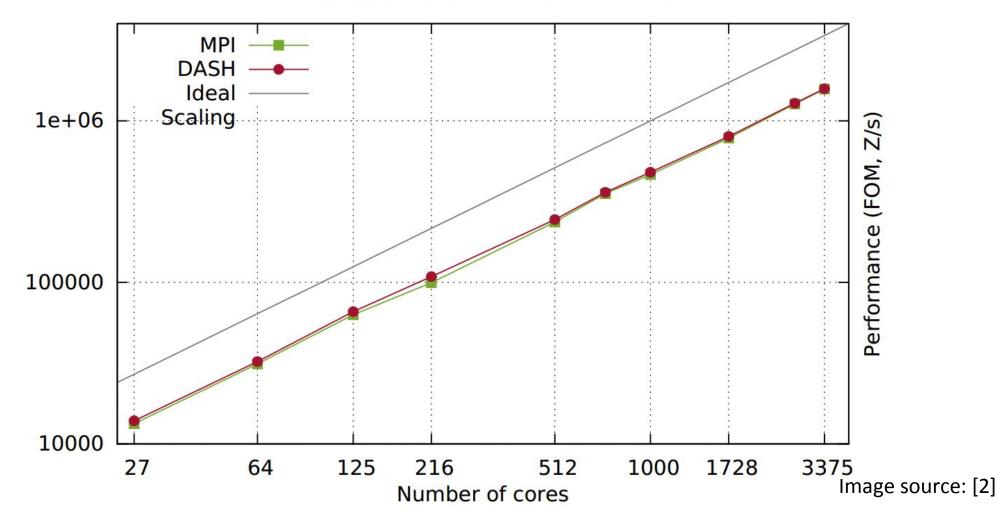
- DASH can be integrated in existing applications and allows for incremental porting
- Porting options:
 - Port data structures, but keep communication as-is (using MPI two-sided)
 - Can use HDF5 writer for checkpointing
 - Keep explicit packing code but use one-sided put instead of MPI_Irecv/MPI_Isend
 - Potential performance benefit from one-sided communication
 - 3. Use DASH for communication directly
 - auto halo = ...; dash::swap(halo) ...
 - less replicated code, more flexibility
 - 4. Use dash::HaloNArray
 - Multi-dimensional array with built-in halo areas



Performance of the DASH Version (using put)

 Performance and scalability (weak scaling) of LULESH, implemented in MPI and DASH

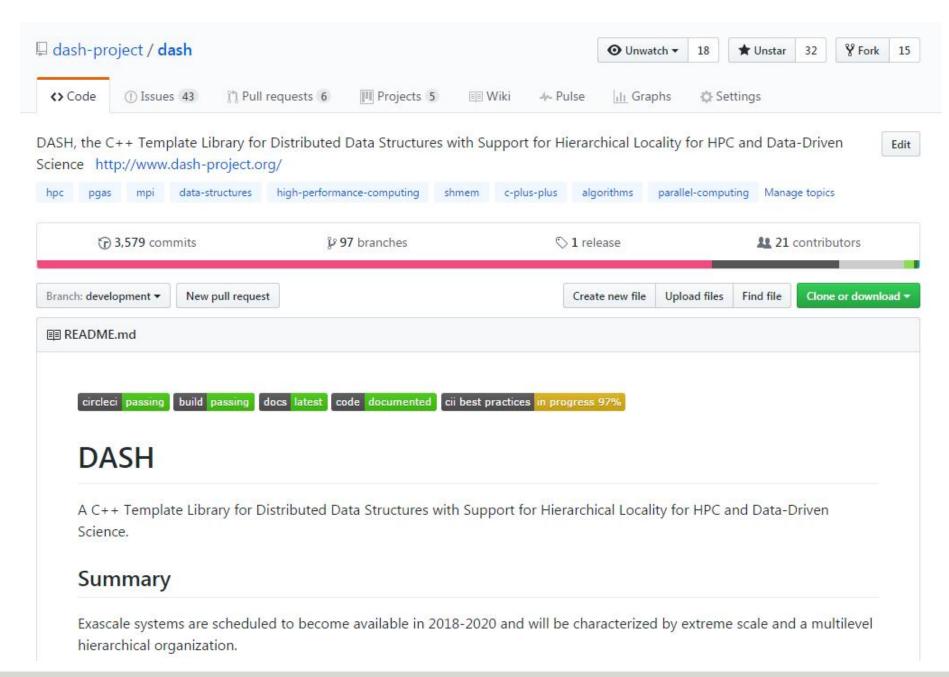
Scaling of LULESH on SuperMUC-HW





DASH on GitHub

https://github.com/dash-project/dash

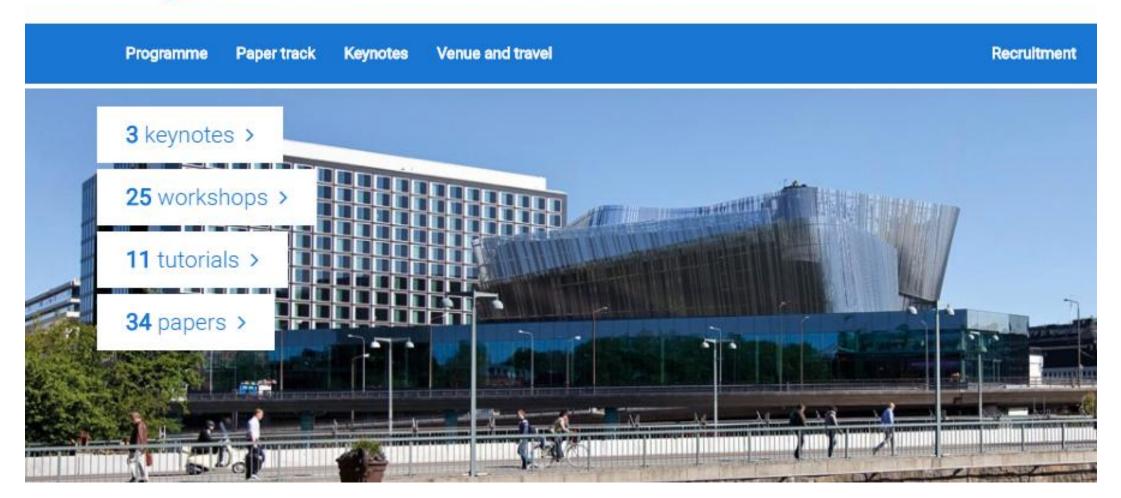




DASH Tutorial

Available at http://www.dash-project.org/tutorial/tutorial.html







DASH On-going Work

- Memory Spaces
 - Support for NVRAM, high-bandwidth memory, replica mgmt
- Task-Based Execution
 - Compute kernels and dependency mgmt
- Dynamic Data Structures (growing/shrinking)
 - List and hashmap as first goals
- Hierarchical Locality Information System
 - Make working with locality easier
- Halo Matrix Smart Data Structure
 - NArray with built-in support for halo exchange



Summary

A The On Gir

DASH is

- A complete data-oriented PGAS programming system (i.e., entire applications can be written in DASH),
- A library that provides distributed data structures (i.e., DASH can be integrated into existing MPI applications)

The DASH Team

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More information

- http://www.dash-project.org/
- https://github.com/dash-project/dash/





References (1)

- [1]: Tobias Fuchs and Karl Fürlinger. A Multi-Dimensional Distributed Array Abstraction for PGAS. In *Proceedings of the 18th IEEE International Conference on High Performance Computing and Communications (HPCC 2016)*, pages 1061-1068. Sydney, Australia, December 2016.
- [2]: Karl Fürlinger, Tobias Fuchs, and Roger Kowalewski. DASH: A C++ PGAS Library for Distributed Data Structures and Parallel Algorithms. In Proceedings of the 18th IEEE International Conference on High Performance Computing and Communications (HPCC 2016), pages 983-990. Sydney, Australia, December 2016.
- [3]: Denis Hünich, Andreas Knüpfer, Sebastian Oeste, Karl Fürlinger, and Tobias Fuchs. **Tool Support for Developing DASH Applications**. In Hans-Joachim Bungartz, Philipp Neumann, and E. Wolfgang Nagel, editors, *Software for Exascale Computing SPPEXA 2013-2015*, pages 361-377. Springer. Garching, Germany, 2016.
- [4]: Roger Kowalewski and Karl Fürlinger. Nasty-MPI: Debugging Synchronization Errors in MPI-3 One-Sided Applications. In *Proceedings of the 22nd International Conference on Parallel and Distributed Computing (Euro-Par 2016)*, pages 51-62. Grenoble, France, 2016.
- [5]: Huan Zhou and José Gracia. Towards performance portability through locality-awareness for applications using one-sided communication primitives. In Forth International Symposium on Computing and Networking, CANDAR Hiroshima, Japan, November 22-25, 2016. IEEE Computer Society, 2016.



References (2)

- [6]: Tobias Fuchs and Karl Fürlinger. Expressing and Exploiting Multi-Dimensional Locality in DASH. In Hans-Joachim Bungartz, Philipp Neumann, and E. Wolfgang Nagel, editors, Software for Exascale Computing SPPEXA 2013-2015, pages 341-359. Springer. Garching, Germany, 2016.
- [7]: Kamran Idrees, Tobias Fuchs, and Colin W Glass. Effective use of the PGAS Paradigm:

 Driving Transformations and Self-Adaptive Behavior in DASH-Applications.

 In Proceedings of the First International Workshop on Program Transformation for Programmability in Heterogeneous Architectures, held in conjunction with the CGO Conference.
- [8]: Huan Zhou and José Gracia. Asynchronous progress design for a MPI-based PGAS one-sided communication system. In 22nd IEEE International Conference on Parallel and Distributed Systems, ICPADS 2016, Wuhan, China, December 13-16, 2016, 2016.